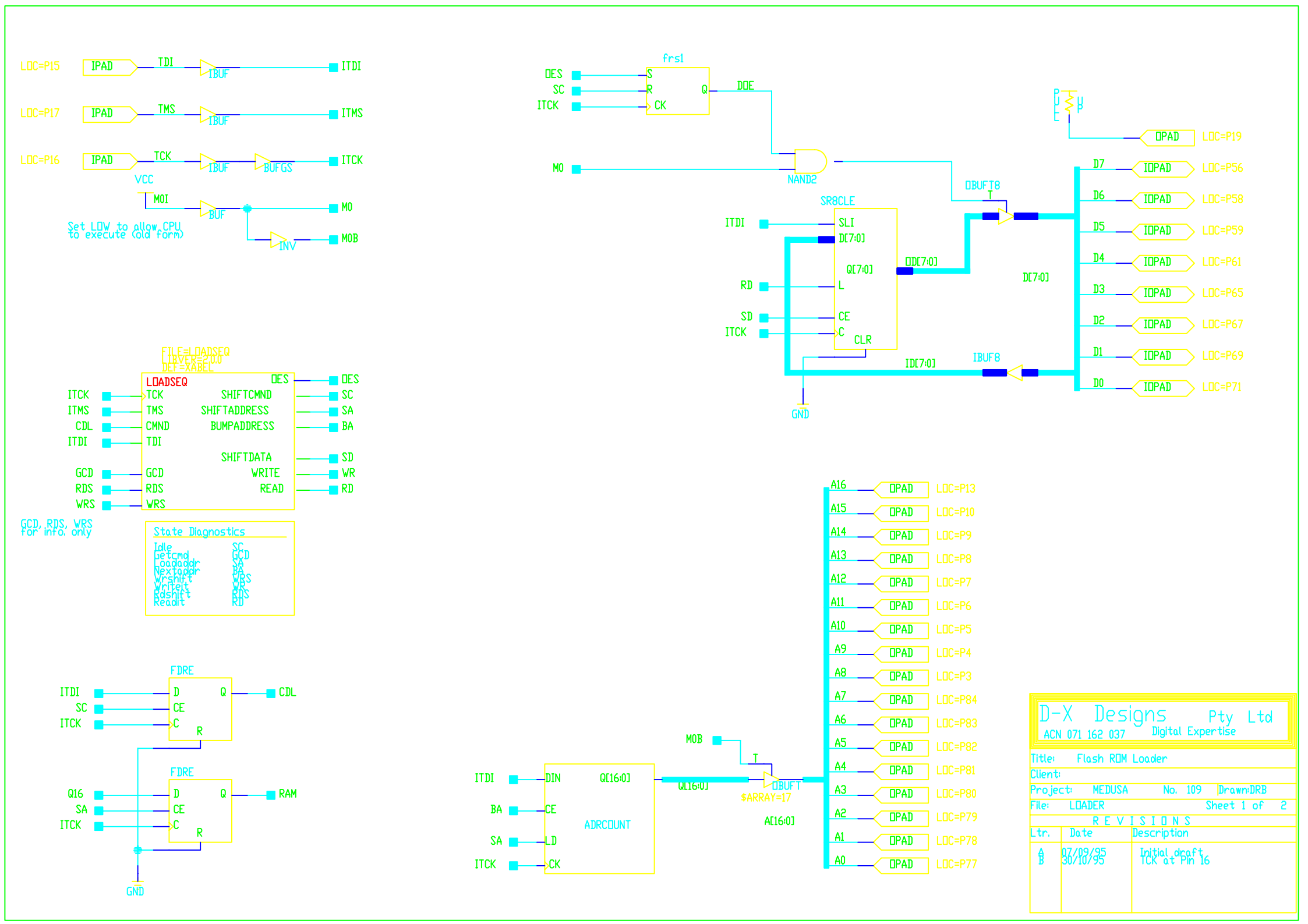
LOC=P15
IPAD
TDI
IBUF
ITDI
LOC=P17
IPAD
TMS
IBUF
ITMS
LOC=P16
IPAD
TCK
IBUF
BUFGS
ITCK
VCC
MOI
BUF
MO
INV
MOB
Set LOW to allow CPU to execute (old form)
frs1
OES
SC
ITCK
S
R
CK
Q
DOE
MO
NAND2
OBUFT8
T
SR8CLE
ITDI
SLI
D[7:0]
Q[7:0]
RD
L
SD
ITCK
CE
C
CLR
OD[7:0]
D[7:0]
ID[7:0]
IBUF8
GND
PULL
OPAD
LOC=P19
D7
IOPAD
LOC=P56
D6
IOPAD
LOC=P58
D5
IOPAD
LOC=P59
D4
IOPAD
LOC=P61
D3
IOPAD
LOC=P65
D2
IOPAD
LOC=P67
D1
IOPAD
LOC=P69
D0
IOPAD
LOC=P71
FILE=LOADSEQ
LIBVER=2.0.0
DEF=XABEL
LOADSEQ
ITCK
ITMS
CDL
ITDI
GCD
RDS
WRS
TCK
TMS
CMND
TDI
GCD
RDS
WRS
OES
SHIFTCMND
SHIFTADDRESS
BUMPADDRESS
SHIFTDATA
WRITE
READ
OES
SC
SA
BA
SD
WR
RD
GCD, RDS, WRS for info. only
State Diagnostics
Idle
Getcmd
Loadaddr
Nextaddr
Wrshift
Wrfeit
Rdshift
Readit
SC
GCD
SA
BA
WRS
WR
RDS
RD
FDRE
ITDI
SC
ITCK
D
CE
C
R
Q
CDL
FDRE
Q16
SA
ITCK
D
CE
C
R
Q
RAM
GND
MOB
T
OBUFT
$ARRAY=17
ITDI
DIN
Q[16:0]
BA
CE
ADRCOUNT
SA
LD
ITCK
CK
Q[16:0]
A[16:0]
A16
OPAD
LOC=P13
A15
OPAD
LOC=P10
A14
OPAD
LOC=P9
A13
OPAD
LOC=P8
A12
OPAD
LOC=P7
A11
OPAD
LOC=P6
A10
OPAD
LOC=P5
A9
OPAD
LOC=P4
A8
OPAD
LOC=P3
A7
OPAD
LOC=P84
A6
OPAD
LOC=P83
A5
OPAD
LOC=P82
A4
OPAD
LOC=P81
A3
OPAD
LOC=P80
A2
OPAD
LOC=P79
A1
OPAD
LOC=P78
A0
OPAD
LOC=P77
D-X Designs Pty Ltd
ACN 071 162 037
Digital Expertise
Title: Flash ROM Loader
Client:
Project: MEDUSA
No. 109
Drawn: DRB
File: LOADER
Sheet 1 of 2
REVISIONS
Ltr.
Date
Description
A
07/09/95
Initial draft
B
30/10/95
TCK at Pin 16