



Panel Switches

S1: Mode

1. IFL
2. Reset
3. Execute SPAD
4. Examine mem.
5. Examine mem.
6. Examine mem.
7. Load mem.

S2: Halt State

1. Microcode box
2. Microcode step
3. Instruction
4. Never

S3: Test Mode

1. Test
2. Normal

Title	Size	Number	Revision
B	11 Dec. 1999		
FILE:	F:\VLSI\micr\complex\complex.dib		Sheet of
			Board D1: